

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

In Re the Application of:

Robert W. Warren, Jr.

Serial No.: 10/767,505

Filed: January 28, 2004

Atty. Docket No.: STL11661/  
390-009-USP

For: METHOD AND SYSTEM FOR  
GENERIC DATA TRANSFER  
INTERFACE

Group Art Unit: 2181

Confirmation No.: 5517

Examiner: Martinez, David E.

**RESPONSE TO NOTICE OF NON-  
COMPLIANT APPEAL BRIEF**

**Mail Stop Appeal Brief - Patents**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

In response to the Notice of Non-Compliant Appeal Brief mailed 26 September 2006, Appellant hereby submits this Paper providing a summary of the claimed subject matter as required by 37 CFR 41.37(c)(1)(v), as set forth in MPEP 1205.03(B).

**V. Summary of Claimed Subject Matter (37 CFR §41.37 (c)(1)(v))**

Independent claim 1 is directed to a host interface (301). *See, e.g.*, Specification at p. 2, lines 8-10 and FIG. 3. The host interface (301) comprises a channel select bit encoder (303) that asserts to a media controller (302) one or more channel select bits. *See, e.g.*, Specification at p. 2, line 10 and FIG. 3. The channel select bits indicate one of a plurality of virtual channels (305-308, 315-322) through which the host interface (301) will communicate over a data bus (311)

with the media controller (302). *See, e.g.*, Specification at p. 2, lines 10-11 and FIG. 3.

Additionally, the host interface (301) comprises a virtual channel controller (304) coupled to the channel select bit encoder (303) that establishes a connection for address-less transfer between the indicated virtual channel of the host interface (301) and a corresponding virtual channel of the media controller (302). *See, e.g.*, Specification at p. 2, lines 12-14, p. 7, lines 5-8, and FIG. 3.

Independent claim 7 is directed to a media controller (302). *See, e.g.*, Specification at p. 2, lines 17-18 and FIG. 3. The media controller (302) comprises a channel select bit decoder (312) that decodes one or more channel select bits received from a host interface (301). *See, e.g.*, Specification at p. 2, lines 18-19 and FIG. 3. The channel select bits indicate one of a plurality of virtual channels through which the host interface (301) will communicate over a data bus (311) with the media controller (302). *See, e.g.*, Specification at p. 2, lines 19-20 and FIG. 3. Additionally, the media controller (302) comprises a virtual channel controller (314) coupled to the channel select bit decoder (312) that decodes the one or more channel select bits and establishes a connection for address-less transfer between the indicated virtual channel of the host interface (301) and a corresponding virtual channel of the media controller (302) selected based on the one or more decoded channel select bits. *See, e.g.*, Specification at p. 2, lines 20-23, p. 7, lines 5-8, and FIG. 3.

Independent claim 15 is directed to a data storage device. *See, e.g.*, Specification at p. 2, lines 24-26 and FIG. 3. The data storage device comprises a host interface (301) and a media controller (302). *See, e.g.*, Specification at p. 2, lines 25-27 and FIG. 3. In addition, the host interface (301) comprises a channel select bit encoder (303) that asserts one or more channel select bits. *See, e.g.*, Specification at p. 2, lines 26-27 and FIG. 3. Also, the channel select bits

indicate one of a plurality of virtual channels (305-308, 315-322) through which the host interface (301) will communicate over a data bus (311). *See, e.g.*, Specification at p. 2, lines 27-28 and FIG. 3. Further, the media controller (302) comprises a channel select bit decoder (312) that decodes one or more channel select bits received from the host interface (301). *See, e.g.*, Specification at p. 3, lines 3-4 and FIG. 3. Also, the media controller (302) comprises a virtual channel controller (314) coupled to the channel select bit decoder (312) that establishes a connection for address-less transfer between the indicated virtual channel of the host interface (301) and a corresponding virtual channel of the media controller (302) selected based on the one or more decoded channel select bits. *See, e.g.*, Specification at page 3, lines 4-7, page 7, lines 5-8, and FIG. 3.

Appellant believes that this Paper remedies any deficiencies noted in the Notice of Non-Compliant Appeal Brief. Accordingly, the Appeal Brief is believed to fully comply with 37 C.F.R. 41.37(c).

A representative from the U.S. Patent and Trademark Office is invited to contact the undersigned at the below-listed telephone number regarding any matters relating to the present application.

Date: 27 October 2008

Respectfully submitted,

/Allison Olenginski/  
Allison Olenginski  
Registration No. 55,509  
Hensley Kim & Holzer, LLC  
1660 Lincoln Street, Suite 3000  
Denver, CO 80264  
(720) 377-0773